

HIDRI Arbi
Embedded Automotive Software Engineer
TELNET Holding
Phone number: +216 96 535 438, E-mail: hidri.arbi@gmail.com
Date of birth: 27/02/1985, Nationality: Tunisian

OBJECTIVE

Seeking company with the potential to expand and grow in which my Embedded Software Engineer skills can be utilized to their fullest.

SUMMARY

Good knowledge in Software Development and Validation **Automotive** and **Avionic** Fields, detail oriented, team-player, and quick-learner; good verbal and written communication skills.

EDUCATION & DEGREES

2010	Master's Degree in Automatic, Signal Processing and Computer Engineering National Engineering School of Tunis (ENIT) Specialization in the areas of embedded systems
2009	National Engineering Diploma in Electrical National Engineering School of Tunis (ENIT) Specialization in the areas of Embedded Software/Hardware Engineering

SKILLS

Automotive	<ul style="list-style-type: none"> Windows, High-performance Embedded Workshop Renesas, IBM Rational Test RealTime (RTRT)., Winades, CAN, KWP-2000, CANalyzer, DiagOnAll, ScanDiag, ScanTool, Simulink, Hitachi H8S 32bits, MISRA-C Eclipse, JAVA, ARXML, A2L, AGS, XML
Avionic & defense	<ul style="list-style-type: none"> Windows, Lauterbach, TRACE32, Diab compiler, Cygwin, Freescale MPC5566, RuleChecker, RS422, ARINC, SPI, GPIO, SSRAM, MRAM, DOORS, CVS, DO178B
Personal Skills	<ul style="list-style-type: none"> Hands-on, ambitious, independent Comprehensive problem solving abilities Ability and desire to learn new technology Good verbal and written communication skills

PROFESSIONAL EXPERIENCE

Since September 2015	<p>Automotive Software Tools Developer: TELNET Holding - Automotive activity, Tunisia. Mission: Development and validation automation testing techniques to enhance the accuracy and reduce the time and cost to testify for automotive embedded software using Eclipse JAVA: Valeo Project.</p> <p>Achievements:</p> <ul style="list-style-type: none"> Design, develop and test SW applications for the automotive domain. Development testing and validation of a tool to generate an Integration Test Plan (ITP) file from Manual Coding Modules: Eclipse, JAVA. Development testing and validation of a tool for executing an Integration Test Plan (ITP), and produces the Integration Test Result (ITR): Eclipse, JAVA. Generating documentation from code sources: Doxygen <p>Technical Environment:</p> <ul style="list-style-type: none"> Scientific tools: Eclipse, Microsoft Visual Studio 10.0, INCA, CANalyzer, MATLAB Simulink Project management tools: Reqtify, SVN, Doxygen
-----------------------------	---

	<ul style="list-style-type: none"> • Specific environments: ARXML, A2L, AGS, XML, OS, CAN • Software development: Java
<p style="text-align: center;">September 2011 - August 2015</p>	<p>Development/Validation Engineer: TELNET Holding - Automotive activity, Tunisia. Mission: Development testing and validation of an Engine Control Unit (ECU): PSA and Valeo Project.</p> <p>Achievements:</p> <ul style="list-style-type: none"> • Develop functionalities in C language and verify MISRA rules « QAC » tool. • Develop unitary tests Scripts. • Implement the software in the ECU using IDEVAID. • Validate each software evolution using WINADES, CANALYZER, CAN. • Test and validation PSA constructor services respecting KWP-2000 with DiagOnAll. <p>Technical Environment:</p> <ul style="list-style-type: none"> • Scientific tools: High-performance Embedded Workshop de Renesas, IBM Rational Test RealTime(RTRT), Winades, Reqtify, CANalyzer, ScanDiag, ScanTool, Simulink • Project management tools: CVS • Specific environments, hardware, norms: OSEK Standard, MISRA-C, CAN, KWP-2000, Hitachi H8S 32bits, CAN caseXL
<p style="text-align: center;">Mars 2010 - August 2011</p>	<p>Avionics Software Engineer: TELNET Holding - Defense Activity, Tunisia (KC-390-OS: SAGEM Project). Mission: Development and testing of a KC-390 Braking Control System (BCS): Embraer military transport aircraft. DO178B-DAL A.</p> <p>Achievements:</p> <ul style="list-style-type: none"> • Analysis and verification of design & specification inputs documentation. • Writing of the document High and Low level 'requirements (HLR & LLR) • Development of critical software and generation of RuleChecker report. • Validate System interfaces and perform HW/SW Integration testing • Generation and analysis of traceability matrices: Reqtify <p>Technical Environment:</p> <ul style="list-style-type: none"> • Scientific tools: Trace 32, µC Power PC MPC5566 • Project management tools: CVS, DOORS • Specific environments, hardware, norms: DO-178B-DAL A, "cygwin" Linux compilation environment emulator (compilateur Diab 5.8), RuleChecker • Communication Networks : ARINC 429 (TX, RX), SPI interface, RS422 • Software development: C programming , TCML (Test Case Modeling Language).
<p style="text-align: center;">August 2009 - Avril 2010</p>	<p>Embedded Software Engineer: National Engineering School of Tunis (ENIT). Mission: Modeling and control of power converters AC-DC, Application to power factor correction.</p> <p>Achievements:</p> <ul style="list-style-type: none"> • Modeling of AC-DC converter: MATLAB, Power Electronics, PID control, BOOST. • Design and implementation of sliding mode control for power factor correction: Simulink • Implementation of control algorithms on the development board : C language, STM32, Keil ARM, STM32VLDISCOVERY • Experience in hardware interface (USB, UART, SPI, I2C),

LANGUAGES

French: Good level on speaking, reading and writing.
English: Good level on speaking, reading and writing.